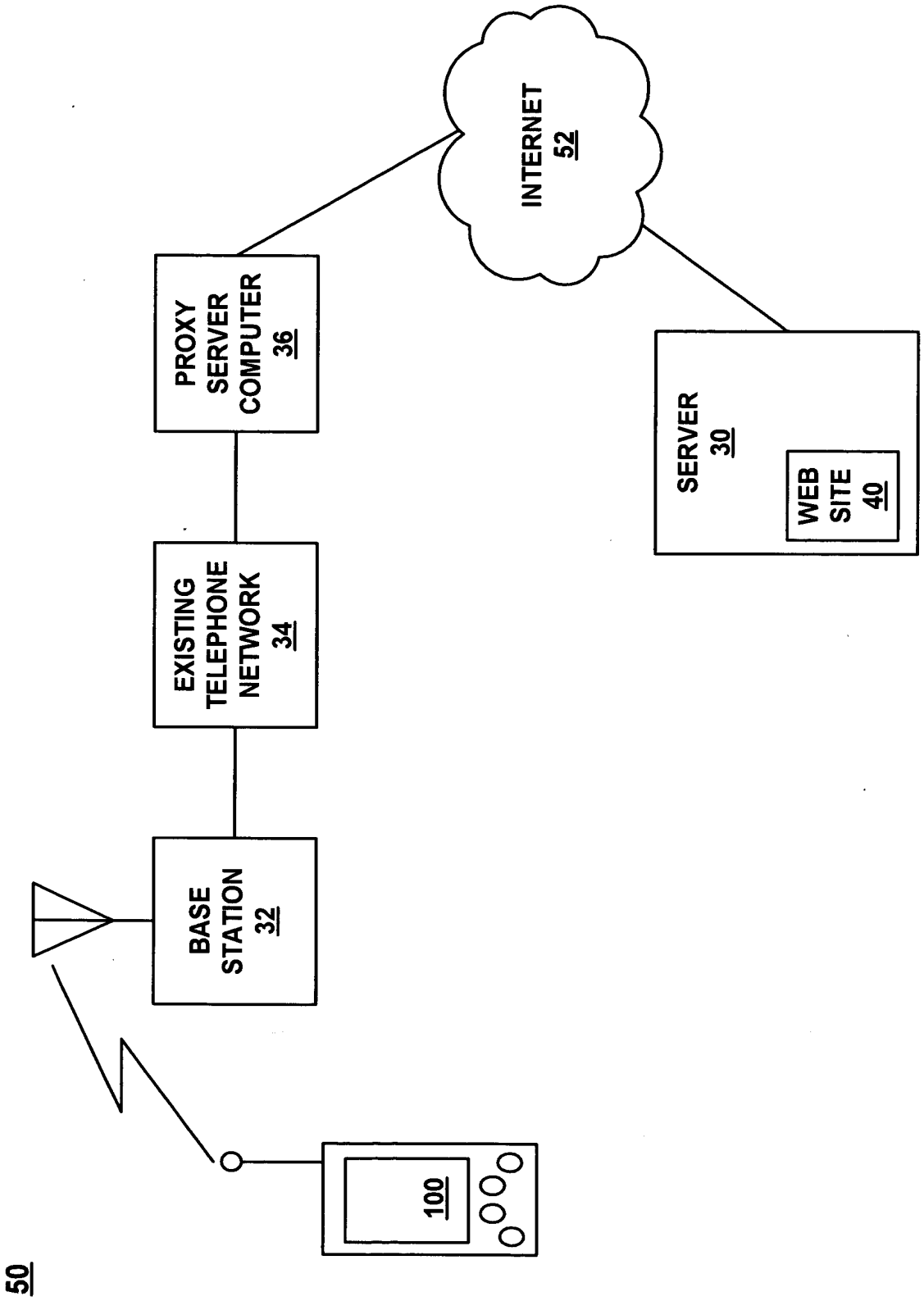
INTERNET
52
PROXY SERVER COMPUTER
36
SERVER
30
WEB SITE
40
EXISTING TELEPHONE NETWORK
34
BASE STATION
32
100
50

FIGURE 1A

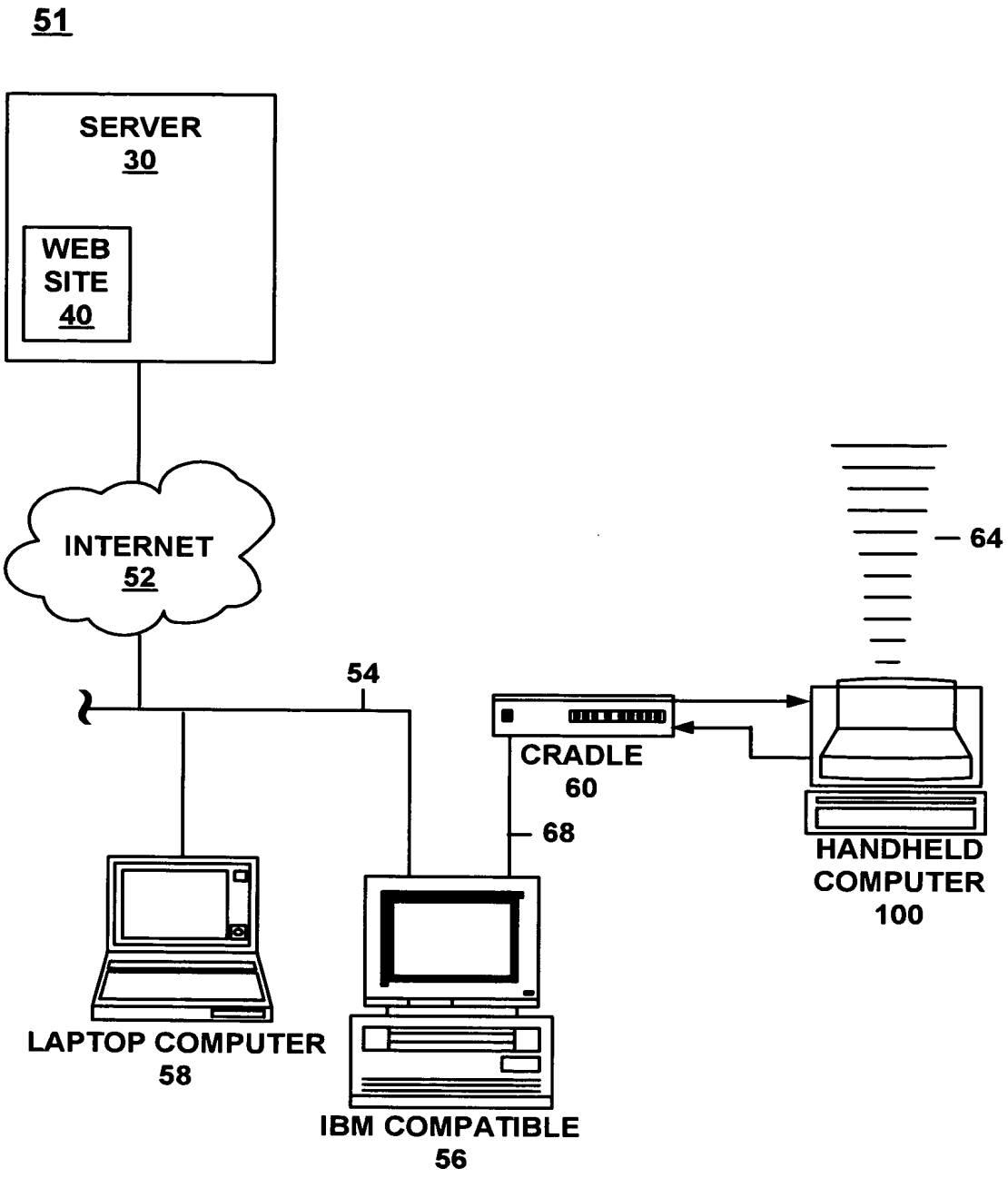
51
SERVER
30
WEB
SITE
40
INTERNET
52
54
64
CRADLE
60
68
HANDHELD
COMPUTER
100
LAPTOP COMPUTER
58
IBM COMPATIBLE
56

FIGURE 1B

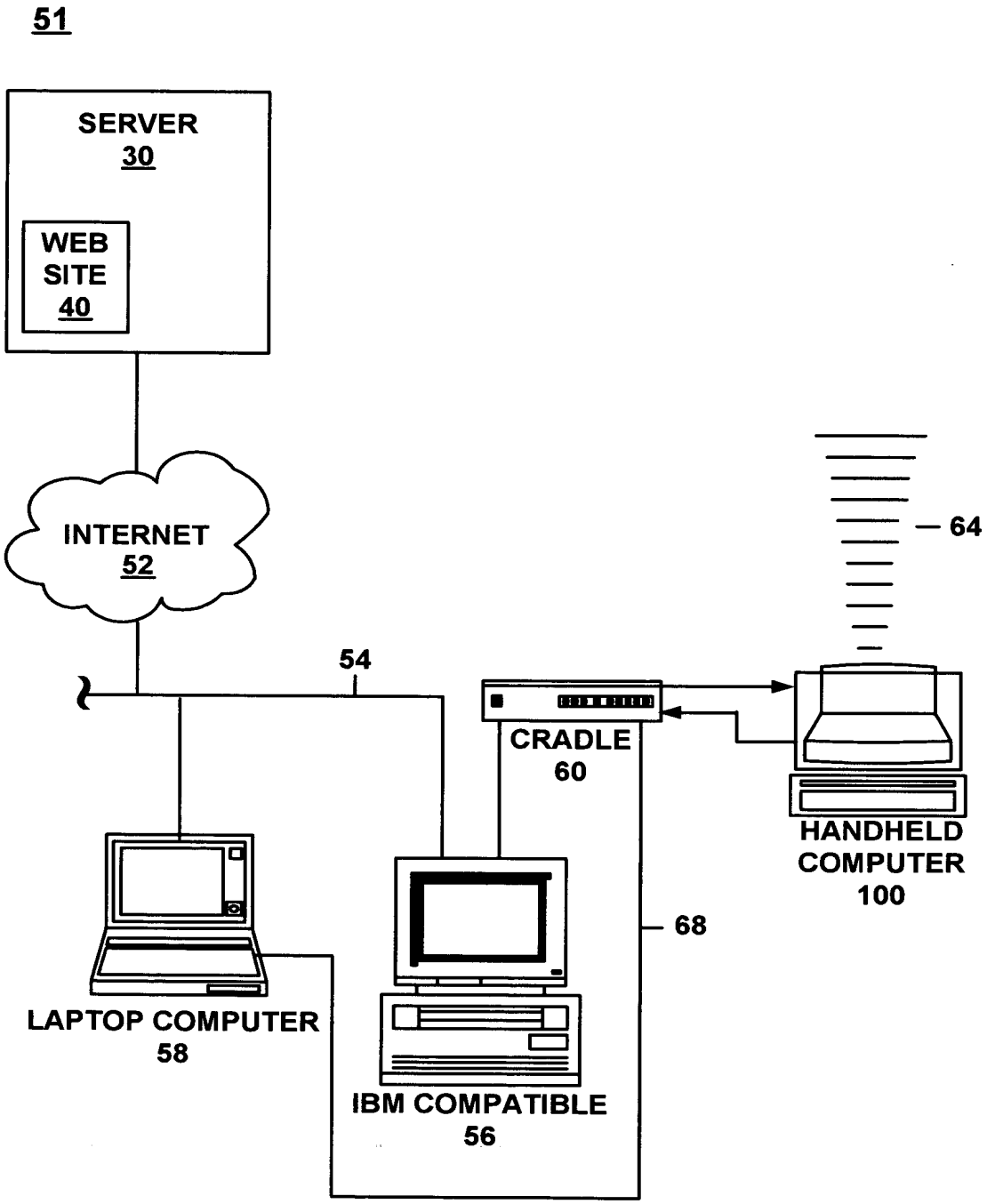
51
SERVER
30
WEB
SITE
40
INTERNET
52
54
64
CRADLE
60
HANDHELD
COMPUTER
100
68
LAPTOP COMPUTER
58
IBM COMPATIBLE
56

FIGURE 1C

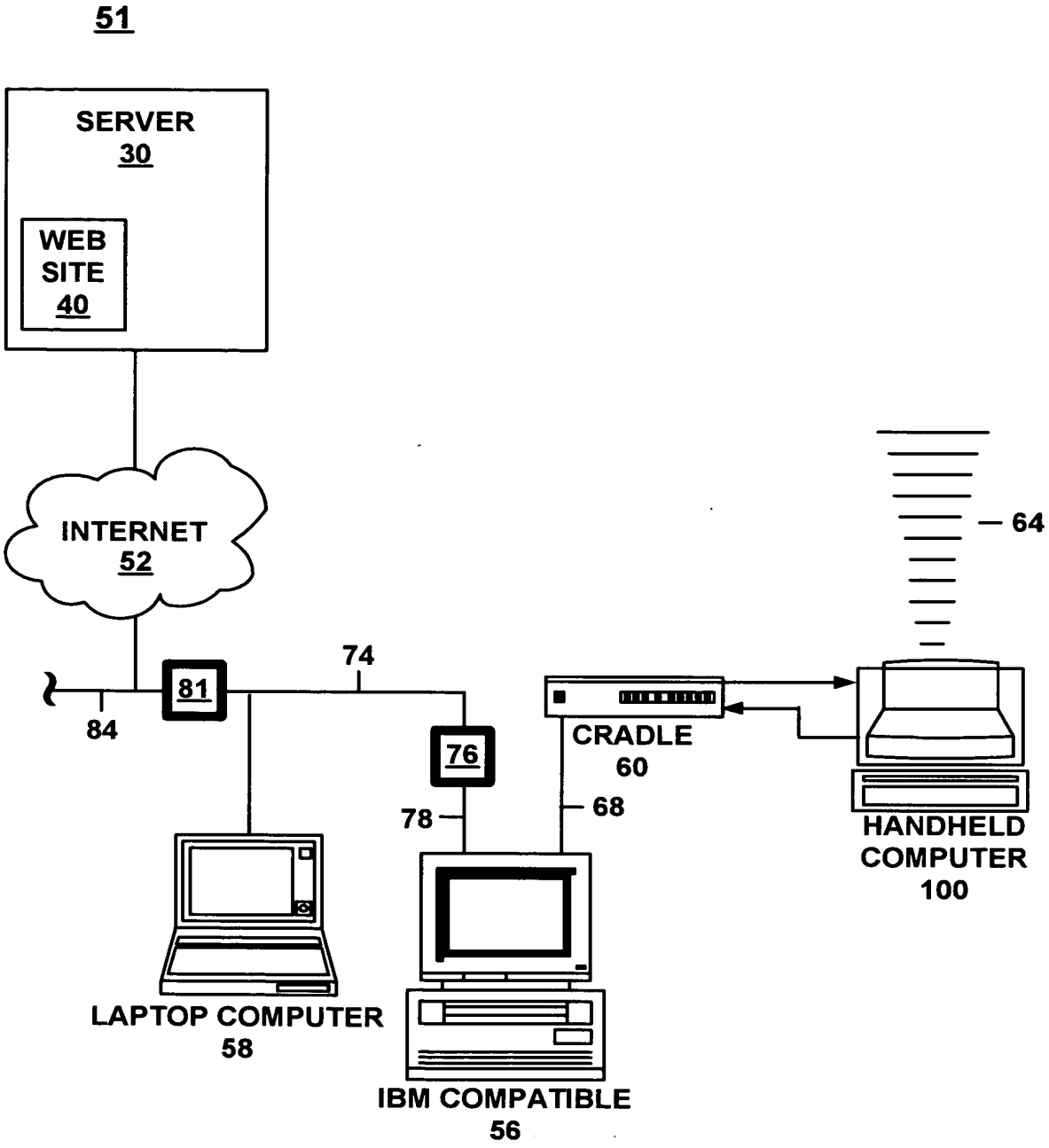
51
SERVER
30
WEB
SITE
40
INTERNET
52
64
74
81
84
76
78
68
CRADLE
60
HANDHELD
COMPUTER
100
LAPTOP COMPUTER
58
IBM COMPATIBLE
56

FIGURE 1D

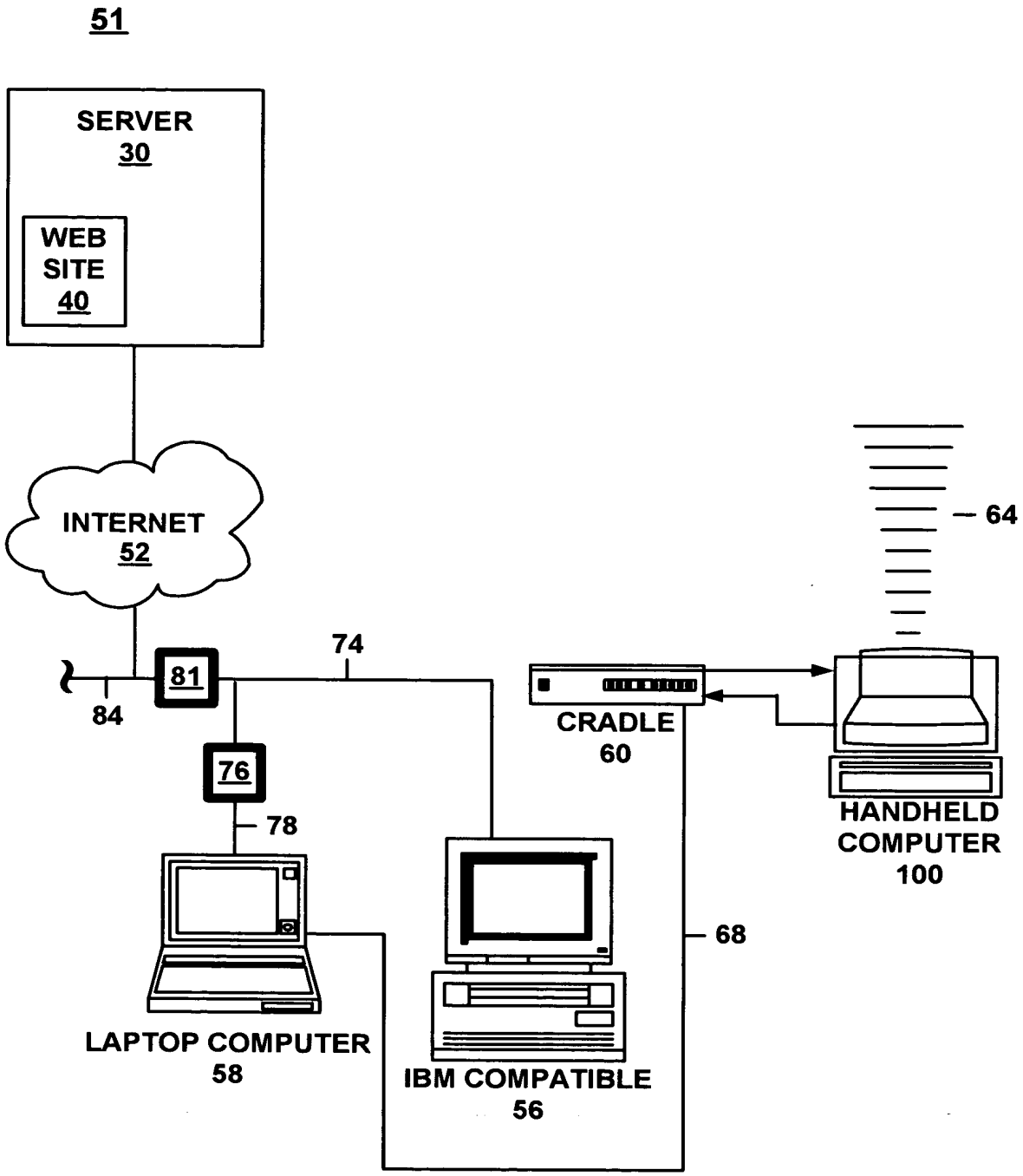
51
SERVER
30
WEB
SITE
40
INTERNET
52
64
74
81
84
76
78
CRADLE
60
HANDHELD
COMPUTER
100
68
LAPTOP COMPUTER
58
IBM COMPATIBLE
56

FIGURE 1E

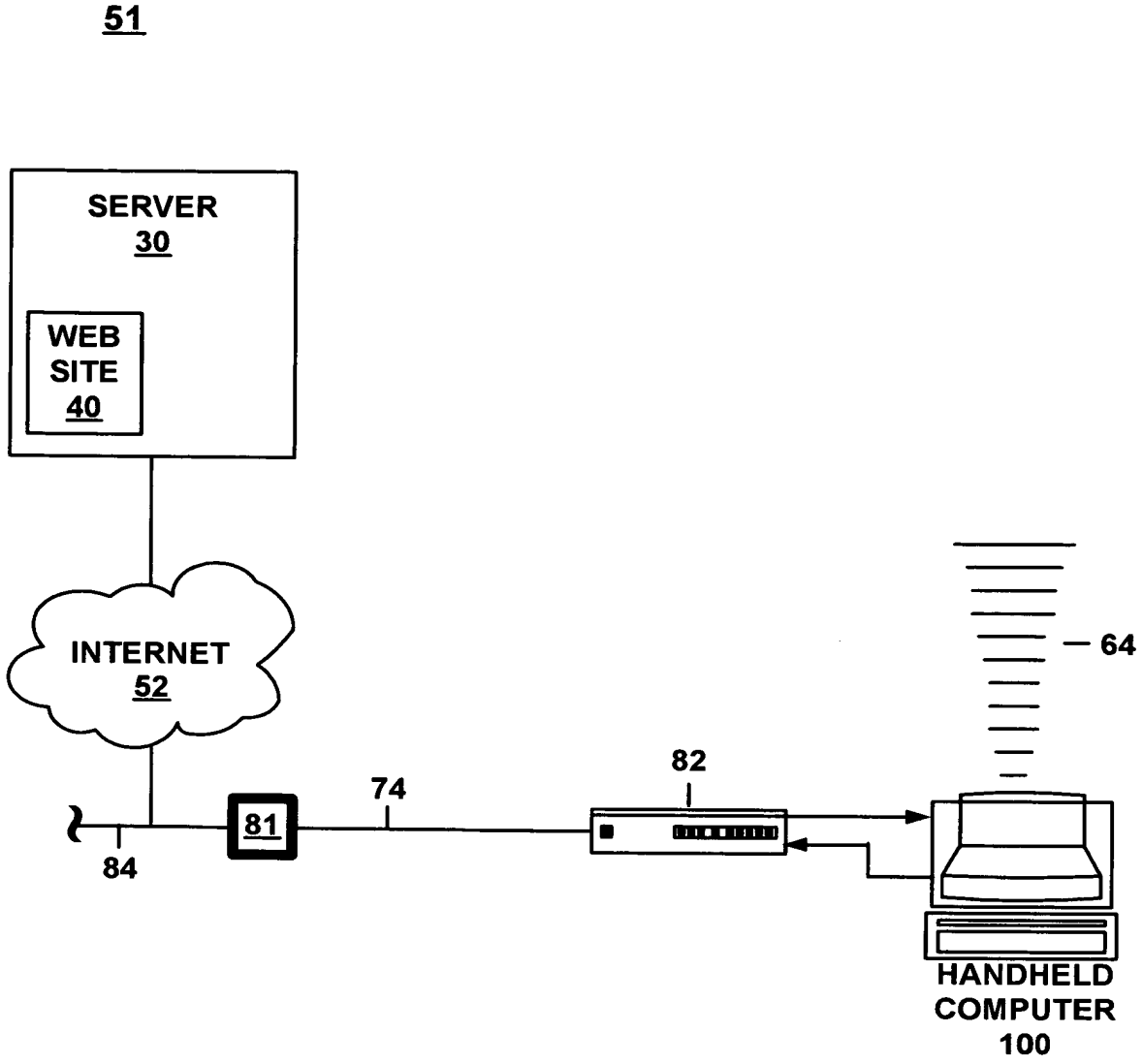
51
SERVER
30
WEB
SITE
40
INTERNET
52
64
82
74
81
84
HANDHELD
COMPUTER
100

FIGURE 1F

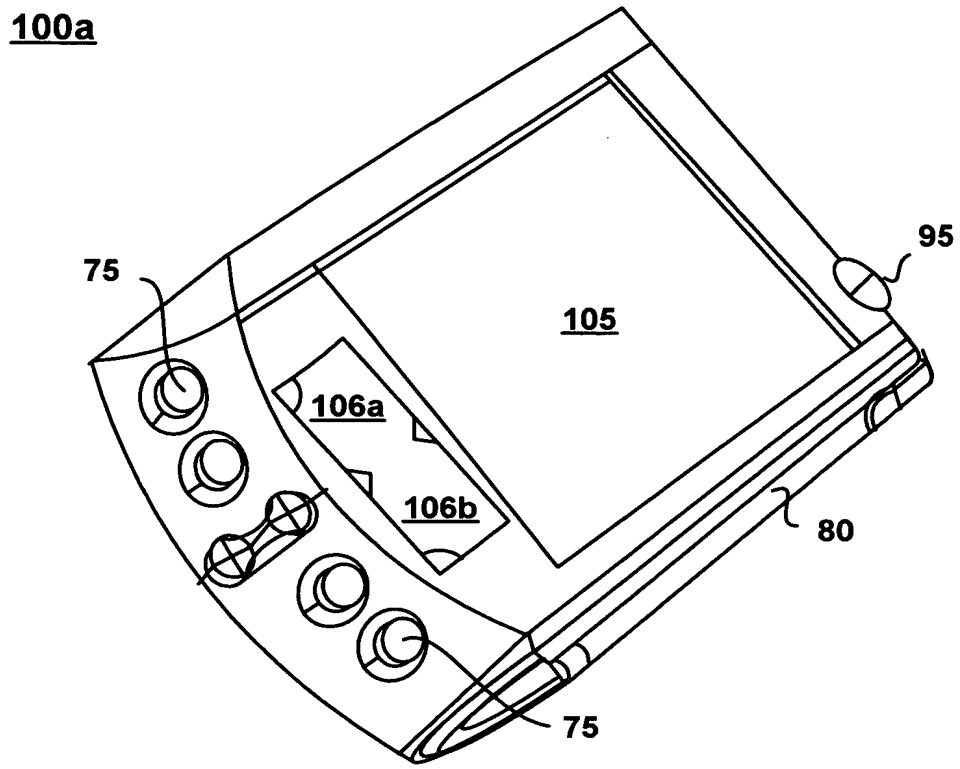

FIGURE 2

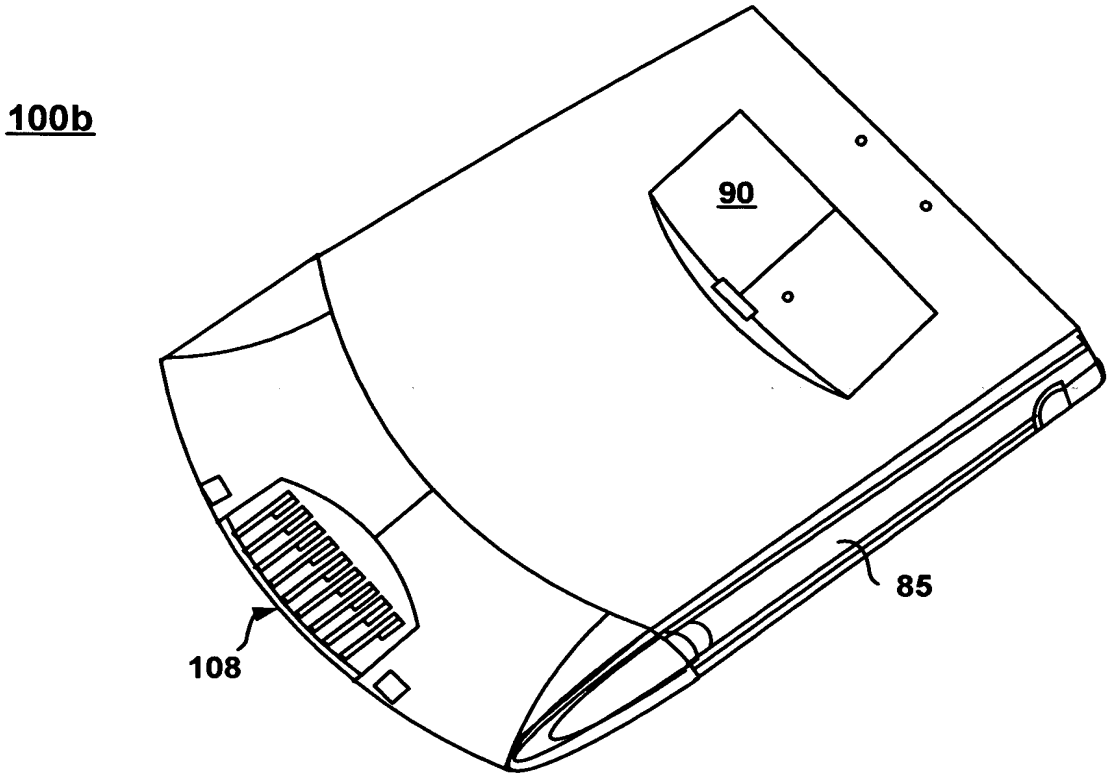

FIGURE 3

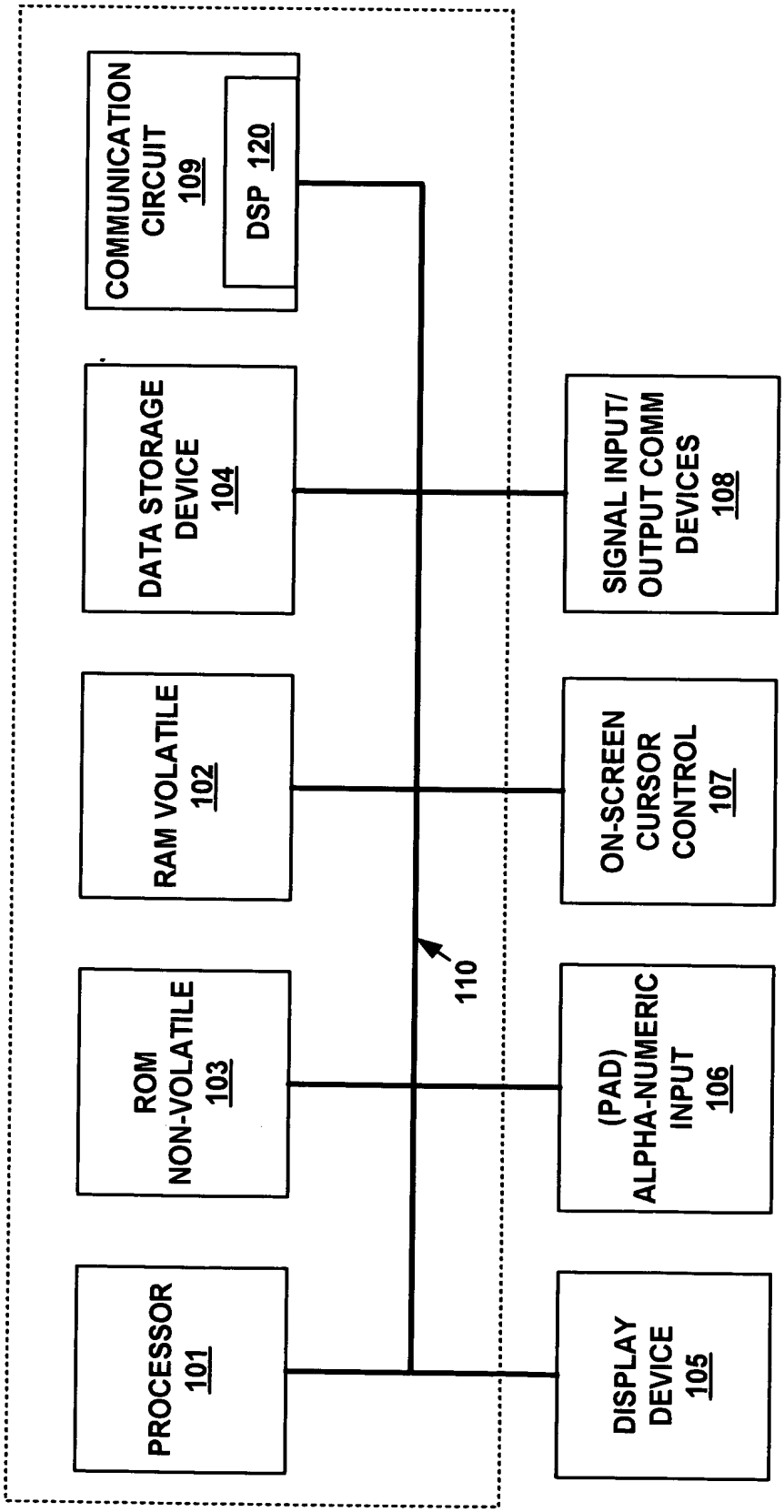
100
COMMUNICATION CIRCUIT 109
DSP 120
DATA STORAGE DEVICE 104
RAM VOLATILE 102
ROM NON-VOLATILE 103
PROCESSOR 101
110
SIGNAL INPUT/ OUTPUT COMM DEVICES 108
ON-SCREEN CURSOR CONTROL 107
(PAD) ALPHA-NUMERIC INPUT 106
DISPLAY DEVICE 105

FIGURE 4

60

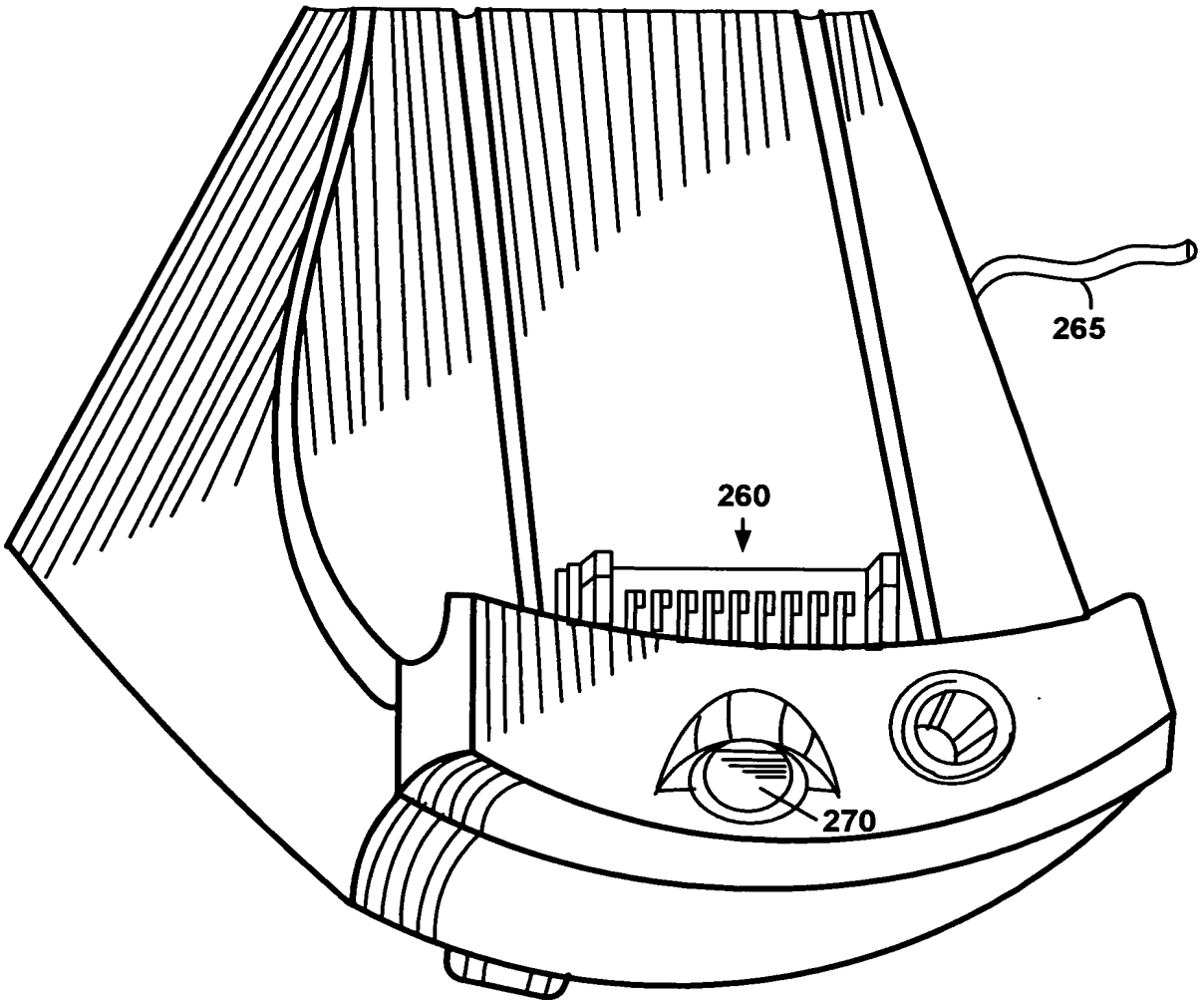

FIGURE 5

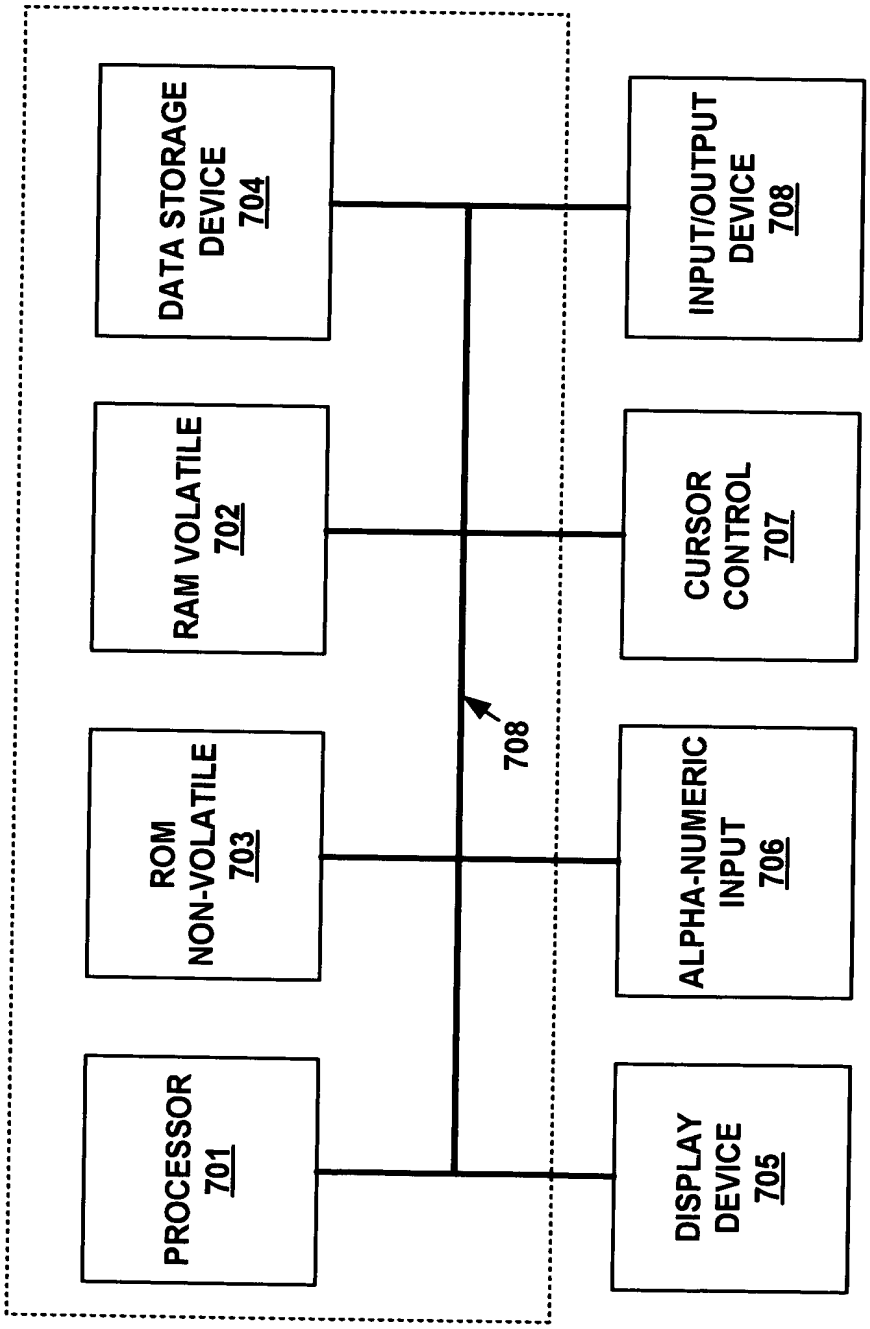
DATA STORAGE DEVICE 704
INPUT/OUTPUT DEVICE 708
RAM VOLATILE 702
CURSOR CONTROL 707
708
ROM NON-VOLATILE 703
ALPHA-NUMERIC INPUT 706
PROCESSOR 701
DISPLAY DEVICE 705
342

FIGURE 6

700

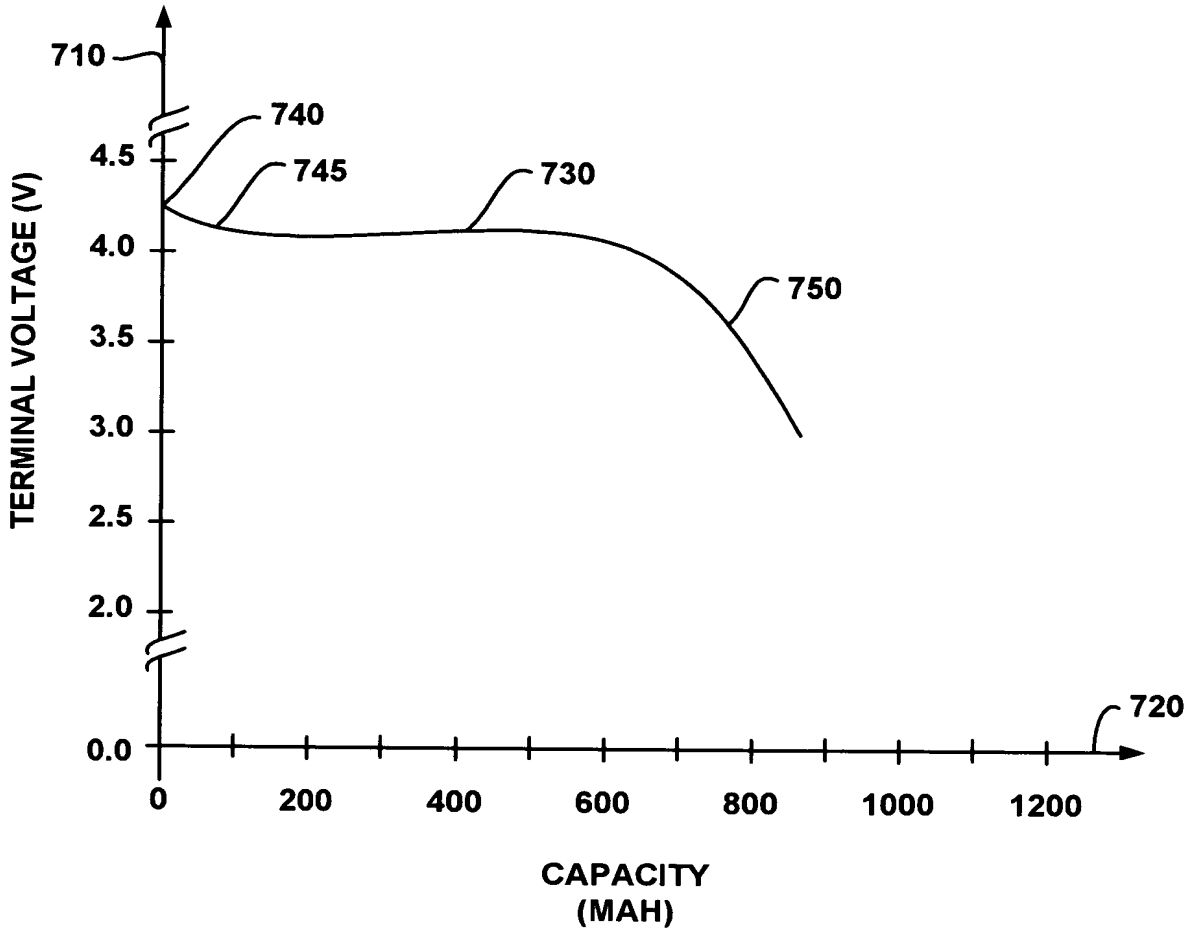
710
740
745
730
750
720
4.5
4.0
3.5
3.0
2.5
2.0
0.0
TERMINAL VOLTAGE (V)
0
200
400
600
800
1000
1200
CAPACITY
(MAH)

FIGURE 7

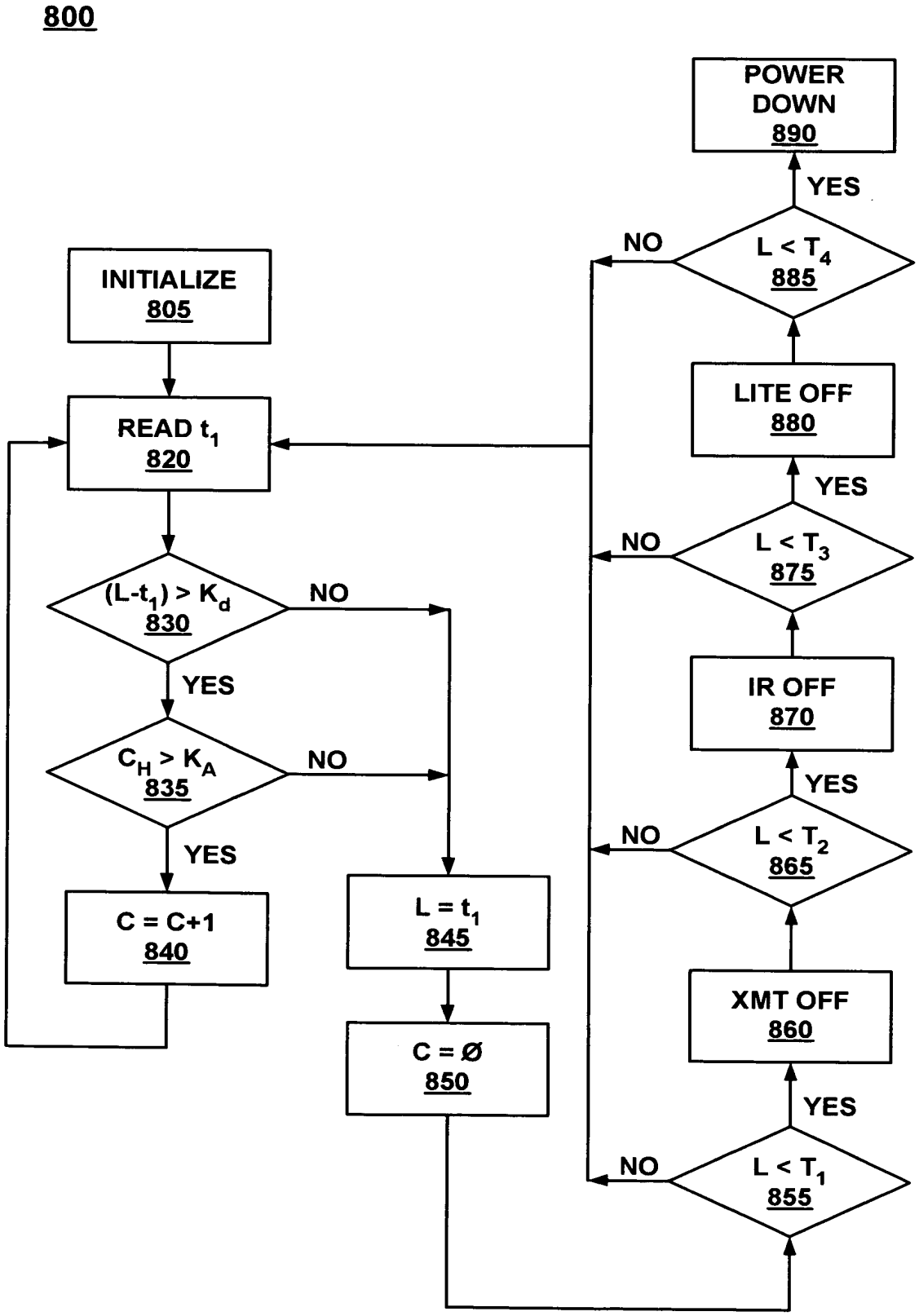
800
INITIALIZE
805
READ t_1
820
$(L-t_1) > K_d$
830
NO
YES
$C_H > K_A$
835
NO
YES
C = C+1
840
$L = t_1$
845
C = Ø
850
POWER DOWN
890
YES
$L < T_4$
885
NO
LITE OFF
880
YES
$L < T_3$
875
NO
IR OFF
870
YES
$L < T_2$
865
NO
XMT OFF
860
YES
$L < T_1$
855
NO

FIGURE 8

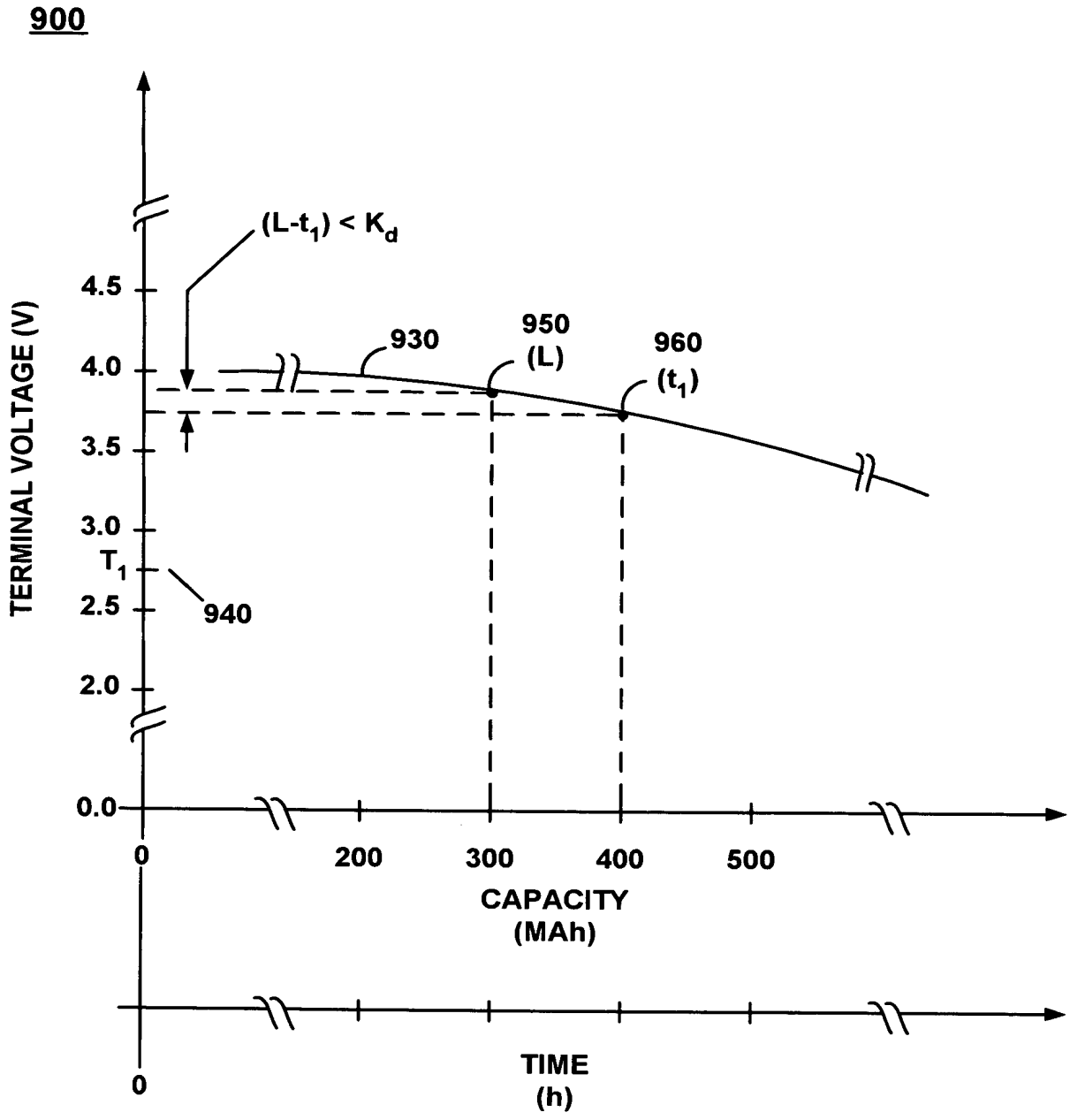
900
$(L-t_1) < K_d$
4.5
4.0
3.5
3.0
T_1
2.5
2.0
0.0
TERMINAL VOLTAGE (V)
930
950
(L)
960
(t_1)
940
0
200
300
400
500
CAPACITY
(MAh)
0
TIME
(h)

FIGURE 9

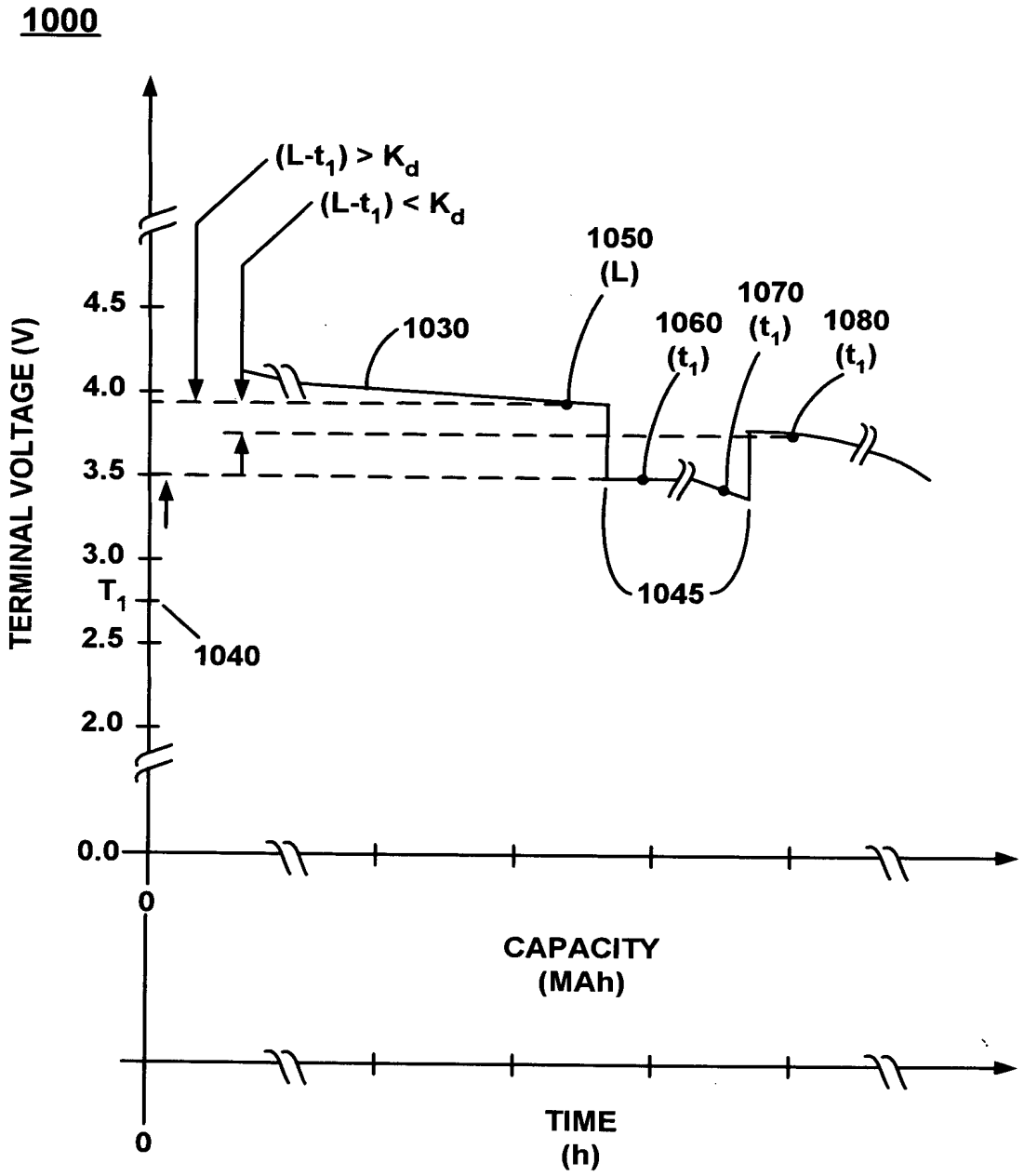
1000
$(L-t_1) > K_d$
$(L-t_1) < K_d$
1050
(L)
1030
1060
(t_1)
1070
(t_1)
1080
(t_1)
4.5
4.0
3.5
3.0
T_1
2.5
2.0
0.0
1040
1045
TERMINAL VOLTAGE (V)
0
CAPACITY
(MAh)
0
TIME
(h)

FIGURE 10

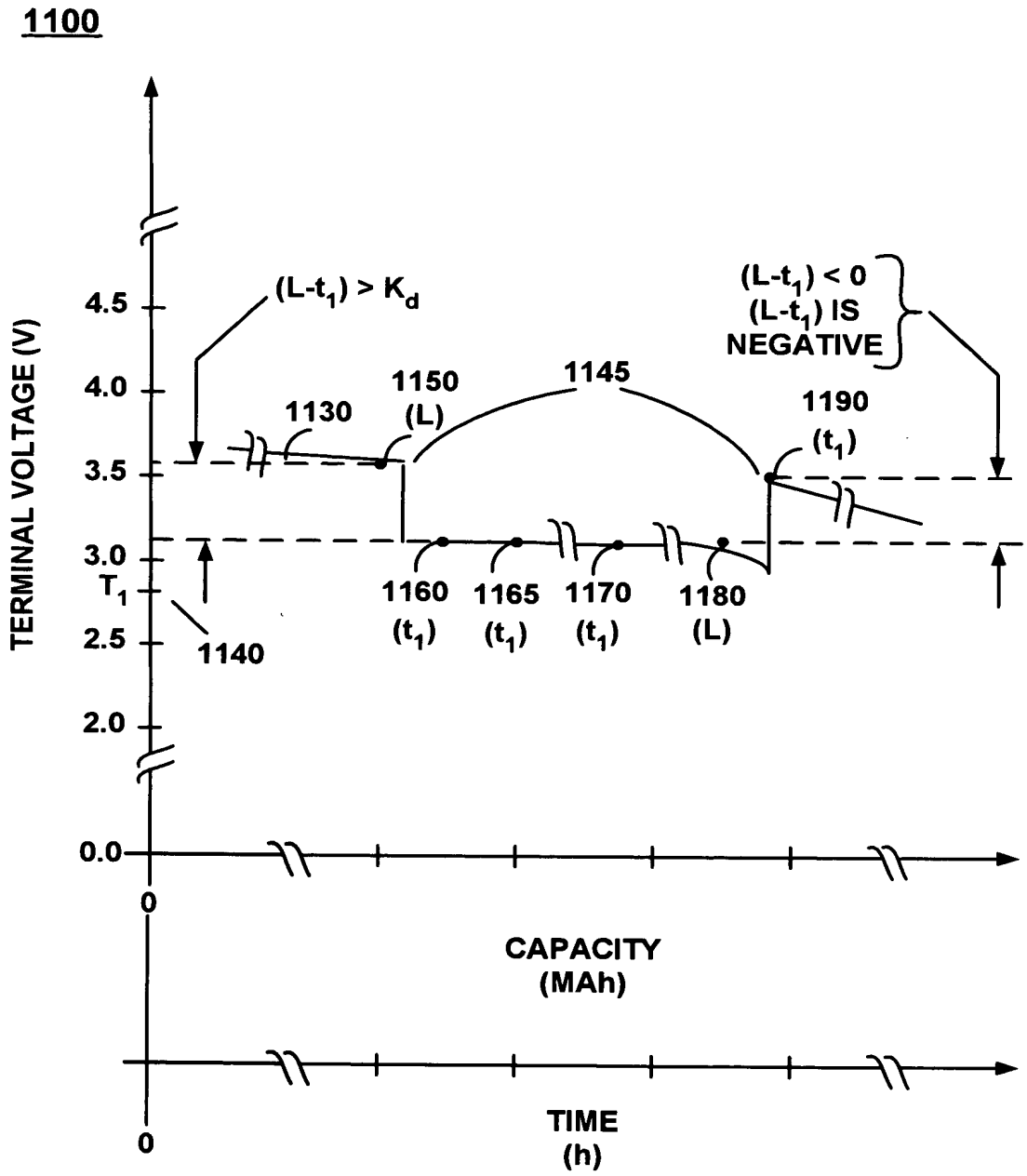
1100
TERMINAL VOLTAGE (V)
4.5
4.0
3.5
3.0
T_1
2.5
2.0
0.0
0
$(L-t_1) > K_d$
$(L-t_1) < 0$
$(L-t_1)$ IS NEGATIVE
1130
1150 (L)
1145
1190 (t_1)
1140
1160 (t_1)
1165 (t_1)
1170 (t_1)
1180 (L)
CAPACITY (MAh)
TIME (h)
0

FIGURE 11

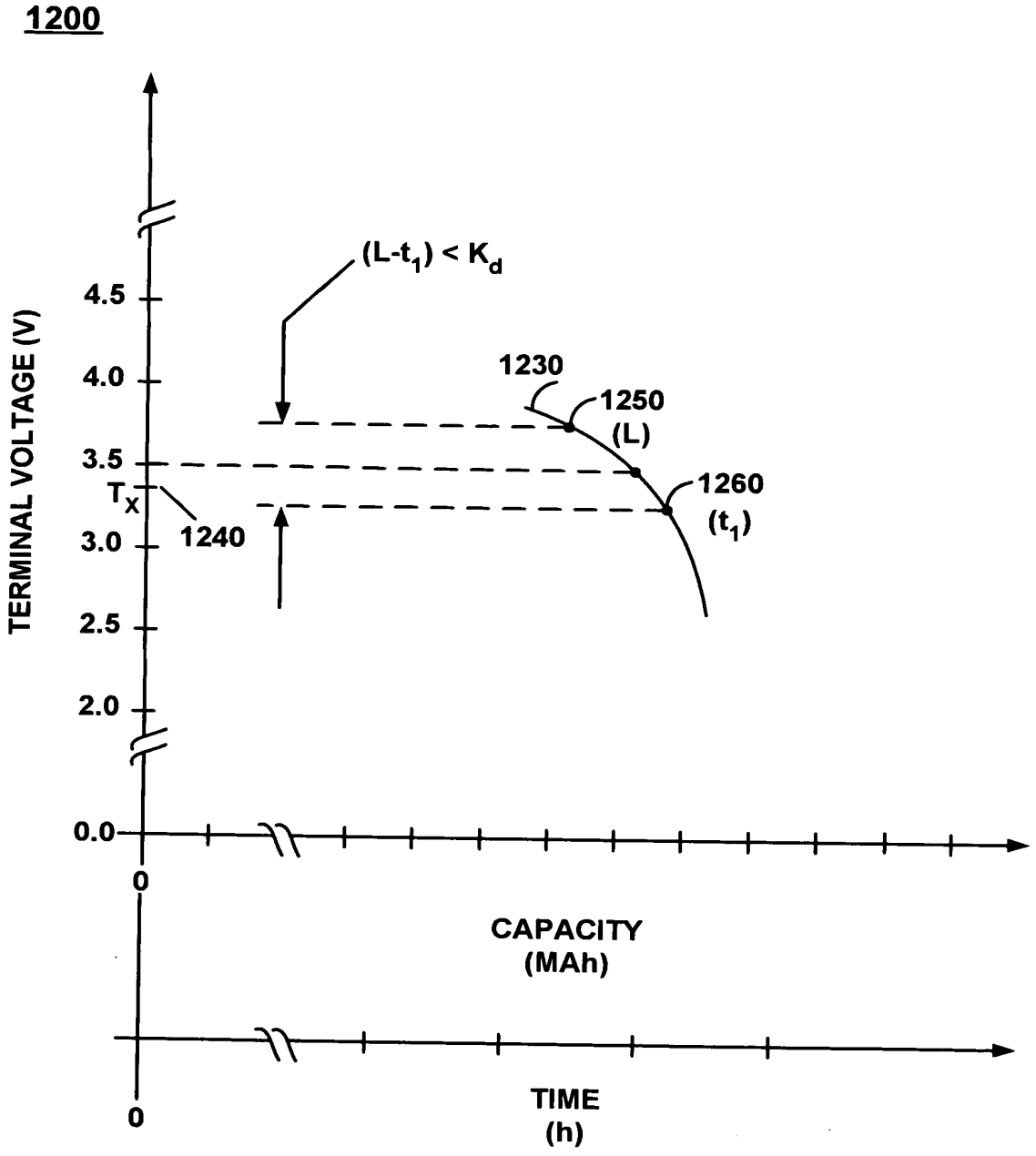
1200
$(L-t_1) < K_d$
4.5
4.0
3.5
T_X
3.0
2.5
2.0
0.0
TERMINAL VOLTAGE (V)
1230
1250
(L)
1260
(t_1)
1240
0
CAPACITY
(MAh)
0
TIME
(h)

FIGURE 12